

Practical Packet Deflection in Datacenters

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Congestion control













Packet Deflection Avoids Drops in the Hotspots!



Deflection: **Re-routing** packets that arrive at a full buffer to a





[Vertigo, CoNEXT '21]

State-of-the-art **deflection** proposals are **not implementable** in existing programmable hardware!



State-of-the-art Deflection Depends on Two Primitives





State-of-the-art Deflection Depends on Two Primitives





Our Contribution:

Implementing Deflection in Programmable Hardware

- Implementing two approaches to deflection:
 - **Simple Deflection**
 - Approximation of Selective Deflection called Preemptive Deflection
- Intuitions: .
 - Using **packet recirculation** instead of expensive memory manipulation. •
 - Using admission control instead of packet extraction from the queue. •



- Using DCTCP Congestion control
- 100 Gbps 8-ary fat-tree cluster
- Incast size of 100 Requests per Query



Approaches to Deflection Suited for Different Needs





What Makes Simple Deflection Hard to Implement





Implementable Simple Deflection in PISA





What Makes Selective Deflection Hard to Implement ${f P}$



Operation Steps

- 1. High-priority packet arrives
- 2. Extract and **deflect** a **low-priority** packet



What Makes Selective Deflection Hard to Implement ${f P}$



Operation Steps

- 1. High-priority packet arrives
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What Makes Selective Deflection Hard to Implement **P**



Operation Steps

- 1. High-priority packet arrives
- 2. Extract and **deflect** a **low-priority** packet
- 3. Insert the new packet at the head of its queue
- 4. The deflected packet experiences **extra hop latency** instead of retransmission!



What Makes Selective Deflection Hard to Implement **P**



Can we use **existing traffic management capabilities** to approximate Selective Deflection?



Implementable Selective Deflection in PISA

Can we use existing traffic management capabilities to approximate **Selective Deflection**?



Deflection Threshold = (τ)

× [1 –

×



Implementable Selective Deflection in PISA

Can we use existing traffic management capabilities to approximate **Selective Deflection**?





Implementable Selective Deflection in PISA

Can we use existing traffic management capabilities to approximate **Selective Deflection**?



- Less accurate than quantile-based deflection



Putting it All Together

Practical Deflection in Datacenters

Simple Deflection

Packet **recirculation** for syncing queue utilization data

Bitmaps for randomly selecting noncongested ports

Quantile-based

Preemptive Deflection

Admission vs deflection policy on FIFO queues Priority comparators parallelized in PISA pipelines

Distribution-based Preemptive Deflection

Using tables prefilled with **statistical distribution** mean-values

- Effective when congestion is infrequent
- Requires minimal resources, no external input

- Can Handle Extreme degrees of congestion
- Accurately approximates selective deflection

- Can handle large degrees of congestion
- Requires few processing stages in Tofino



Implementable Deflection Improves the Performance





Implementable Deflection Under Large-scale Incast





Implementable Deflection Under Large-scale Incast





Implementable Deflection Under Large-scale Incast





We Made Packet Deflection Practical

- We propose an accurate implementation of **Simple Deflection** on PISA architecture.
- We introduce **Preemptive Deflection**, an approximation of selective deflection on PISA.
- Choosing among deflection techniques depends on:
 - Network utilization & congestion intensity
 - Resource availability
 - Performance requirements
- Preemptive Deflection improves high-priority Flow Completion Times by **425x** in a physical testbed.
- Visit https://hopnets.github.io/practical_deflection for the codebase
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Backup slides

